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Company Name	MegaChips Corporation
Representative	Tetsuo Hikawa, President and CEO (Code number: 6875, Tokyo Stock Exchange)
Inquiries	Masahisa Miyake, Corporate Planning Office (Tel: +81-6-6399-2884)

MegaChips and proteanTecs Collaborate on Next-Generation ASIC and LSI Design

～Enhancing Visibility, Reliability, and Performance from Production to In-Mission Operation～

MegaChips Corporation (the “Company”) is collaborating with proteanTecs Ltd. (proteanTecs), the leading provider of health and performance management solutions for advanced electronics, to bring its in-chip monitoring and deep data analytics technologies to the Company’s next-generation ASIC and LSI designs.

Through this collaboration, the two companies will provide “deep silicon visibility” for advanced ASIC and LSI designs used in AI and high-performance electronic systems, enabling detailed insights into semiconductor behavior throughout the product lifecycle, from development and production to in-mission operation.

During production, data generated by in-chip monitors will provide insights into silicon health and performance, supporting yield optimization, quality improvement, and enhanced reliability. During in-mission operation, the same monitoring and analytics capabilities will provide visibility into actual device behavior under real workloads and operating conditions, supporting power and performance management, reliability monitoring, and early identification of potential issues.

By combining proteanTecs’ in-chip monitoring and data analytics technologies with the Company’s ASIC and LSI design expertise, the two companies will provide customers with actionable silicon insights throughout the product lifecycle, supporting faster bring-up, power and performance management, improved reliability, and better visibility into device behavior during in-mission operation.

In markets such as AI, edge computing, data centers, and advanced communications, semiconductor devices are facing increasing demands for higher performance, power efficiency, thermal performance, and reliability. At the same time, as semiconductor devices become increasingly miniaturized and designs grow more complex, visibility into actual silicon behavior during production and in-mission operation is becoming increasingly important.

By combining the ASIC and LSI design expertise and design-for-test (DFT) technologies that the Company has cultivated over many years with proteanTecs’ in-chip monitoring and data analytics technologies, the Company will support customers in reducing development and production risks, shortening time to market, improving product reliability, and optimizing power and performance.

Through this collaboration, the two companies will support the optimization of yield, power consumption, performance, and reliability in next-generation ASIC and LSI designs, including AI semiconductors and Co-Packaged Optics (CPO), which are expected to become increasingly sophisticated and deliver higher levels of performance.

Through this collaboration, the Company and proteanTecs will advance semiconductor solutions that enable customers to better understand and manage the health and performance of next-generation ASIC and LSI devices throughout their lifecycle, from production to in-mission operation, supporting applications in growth markets such as AI, data centers, edge computing, and advanced communications.

About MegaChips

Established in 1990 as Japan's first fabless LSI company, MegaChips Corporation develops innovative semiconductor solutions by combining analog and digital technologies.

Building on its extensive expertise in LSI design accumulated over many years, the Company provides a broad range of solutions, including ASICs, ASSPs, and module solutions, supporting customers' diverse needs in global markets from design through manufacturing.

Website: www.megachips.co.jp/english/

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